

Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
S52	66	(synchron\$4 concurrent\$4 simultan\$6 parallel\$4 isochronal\$4 ("SAME" adj2 time)) same ((replace\$5 flush\$3 evict\$3) with (way bank set column)) same (direct\$3 near3 (access\$4 read\$3 writ\$3 stor\$3 load\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:21
S53	42	(synchron\$4 concurrent\$4 simultan\$6 parallel\$4 isochronal\$4 ("SAME" adj2 time)) same ((replace\$5 flush\$3 evict\$3) with (way bank set column)) same (overrid\$4 overwrit\$3 (over near2 (writ\$3 stor\$3)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:21
S54	827	(synchron\$4 concurrent\$4 simultan\$6 parallel\$4 isochronal\$4 ("SAME" adj2 time)) same ((replace\$5 flush\$3 evict\$3) with (way bank set column)) and (overrid\$4 overwrit\$3 ((over forc\$3) near2 (writ\$3 stor\$3)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:21
S55	45	((flush\$3 replac\$3 evict\$3) with miss with (entry line data way set column bank block)) and ((overrid\$3 forc\$3 over-writ\$3 over-read\$3 over-load\$3 over-stor\$3) same (synchron\$4 parallel\$3 concurrent\$3 simultan\$5 isochron\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:22
S56	5	((flush\$3 replac\$3 evict\$3) with miss with (entry line data way set column bank block)) same (overrid\$3 forc\$3 over-writ\$3 over-read\$3 over-load\$3 over-stor\$3) same (synchron\$4 parallel\$3 concurrent\$3 simultan\$5 isochron\$4)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:22
S57	45	((flush\$3 replac\$3 evict\$3) with miss with (entry line data way set column bank block)) and ((overrid\$3 forc\$3 over-writ\$3 over-read\$3 over-load\$3 over-stor\$3) same (synchron\$4 parallel\$3 concurrent\$3 simultan\$5 isochron\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:26

S58	75	((flush\$3 replac\$3 evict\$3) with miss\$3 with (entry line data way set column bank block)) and ((overrid\$3 forc\$3 over-writ\$3 over-read\$3 over-load\$3 over-stor\$3) same (synchron\$4 parallel\$3 concurrent\$3 simultan\$5 isochron\$4))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:26
S59	72	(cach\$3 memory sdram storage) and S58	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:28
S60	1	("711"/\$.ccls. and "714"/\$.ccls.) and S58	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:29
S61	3867	(replac\$3 flush\$3 evict\$3) with (dirty invalid\$3 miss\$3) with (entry line address column data set way bank)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:30
S62	348	(concurrent\$4 parallel\$4 synchron\$5 simultan\$6 isochrona\$3 contemporan\$6) same S61	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:31
S63	5067	(overrid\$4 overwrit\$3 overstor\$3 ((over forc\$4) near3 (writ\$3 stor\$3 load\$3 read\$3 access\$3))) same (direct\$3 near4 (access\$3 transact\$3 load\$3 read\$3 writ\$3 stor\$3)) same (cach\$3 storage memory sdram)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:39
S64	23	S62 and S63	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:44
S65	18852	(overrid\$4 overwrit\$3 overstor\$3 ((over forc\$4) near3 (writ\$3 stor\$3 load\$3 read\$3 access\$3))) same (direct\$3 near4 (access\$3 transact\$3 load\$3 read\$3 writ\$3 stor\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:39
S66	5067	S63 and S65	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:40
S67	23	S64 and S65	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:40

S68	24	S62 and S65	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:40
S69	49297	"7:14"/\$.ccls.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:41
S70	453	S65 and S69	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:41
S71	5067	S65 and S63	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:41
S72	330	S70 and S63	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:41
S73	3	S62 and S72	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:42
S74	453	S65 and S69	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:42
S75	3	S62 and S74	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:43
S76	2	"6535958".pn.	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:45
S77	1	S62 and S76	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:46
S78	18	((replace\$3 flush\$3 evict\$4) with (miss\$3 dirty invalid\$4)) same (concurrent\$3 parallel\$4 simultan\$4 synchron\$4 isochron\$5) same ((overrid\$3 overwrit\$3 (over forc\$3 permit\$4) near4 (writ\$3 stor\$3 load\$3 read\$3 access\$3)))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:51

S79	11	((replace\$3 flush\$3 evict\$4) with (miss\$3 dirty invalid\$4)) same (concurrent\$3 parallel\$4 simultan\$4 synchron\$4 isochron\$5) same ((next another subsequen\$4) near4 (writ\$3 stor\$3 load\$3 read\$3 access\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:54
S80	6	((replace\$3 flush\$3 evict\$4) with (miss\$3 dirty invalid\$4)) same (concurrent\$3 parallel\$4 simultan\$4 synchron\$4 isochron\$5) same ((direct or directly) near3 (access\$3 read\$3 load\$3 stor\$3 writ\$3))	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/08/31 20:55
S81	315	(replac\$4 flush\$4 evict\$4 victim writeback storeback copyback ((stor\$3 writ\$3 cop\$3) near2 back)) same (miss\$4 invalid\$4 dirty) same (concurrent\$4 parallel\$4 synchron\$6 simultan\$6 ("SAME" adj time)) same (another next subsequent\$4) same (access\$4 read\$3 writ\$4 load\$3 stor\$3)	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/01 07:42
S82	244	(forc\$5 permit\$4 overrid\$4 overwrit\$3 overstor\$3 (over near2 (writ\$3 stor\$4))) and S81	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/01 07:43
S83	60	((forc\$5 permit\$4 overrid\$4 overwrit\$3 overstor\$3 (over near2 (writ\$3 stor\$4))) with (hit\$4 miss\$4)) and S81	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/01 07:43
S84	48	((directly or direct) near4 (access\$4 transact\$4 writ\$3 read\$3 load\$3 stor\$3)) and S83	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/01 07:50
S85	3	"711"/\$.ccls. and "714"/\$.ccls. and S83	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/01 07:51
S86	37	"711"/\$.ccls. and S83	US-PGPUB; USPAT; EPO; JPO; DERWENT	OR	ON	2005/09/01 07:51